

FIG. 1 (Related Art)

Optimization of Application Program Requirements Using Multiple ISAs

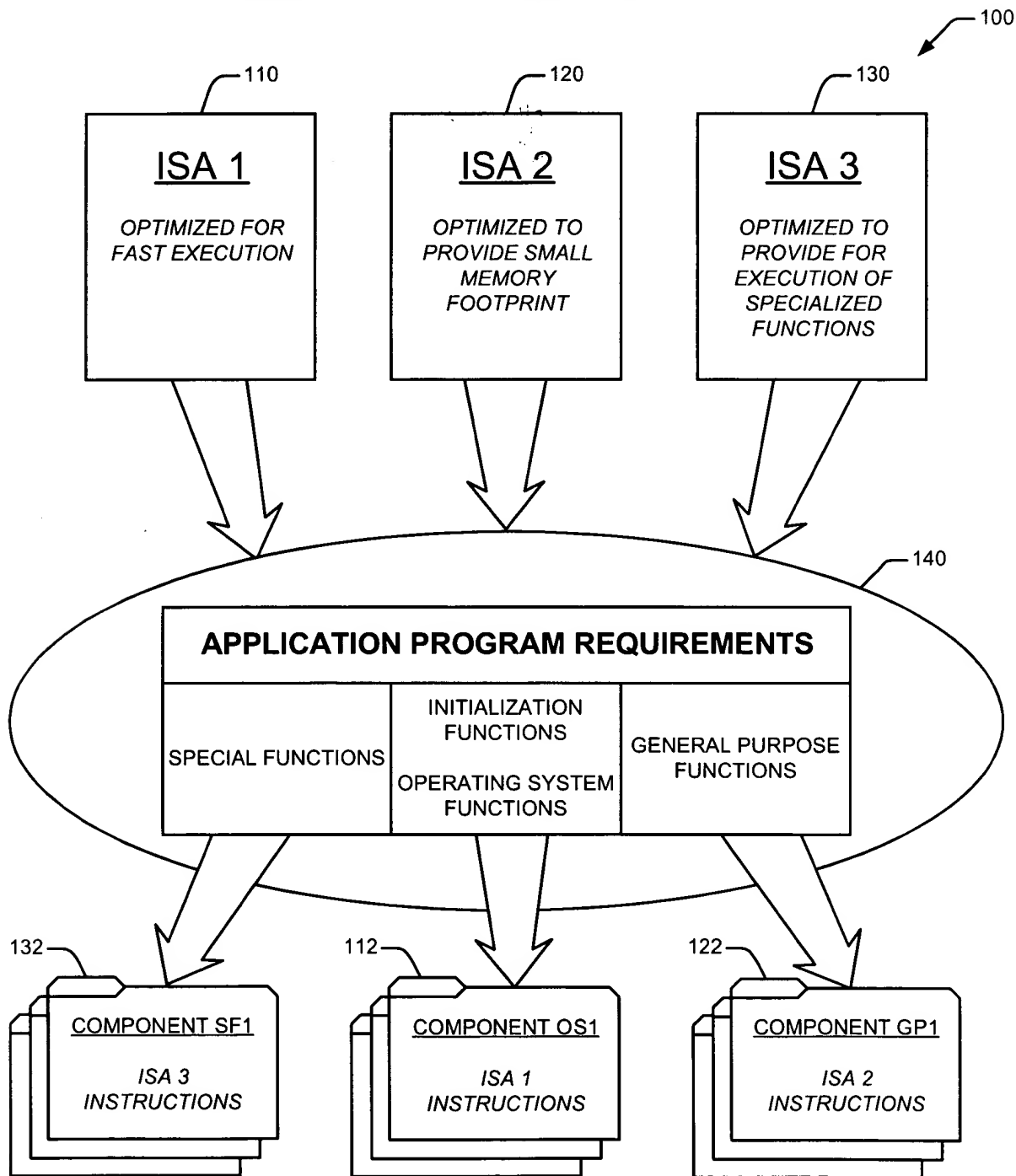


FIG. 2 (Related Art)

Decoding and Executing Multiple-ISA Application Programs

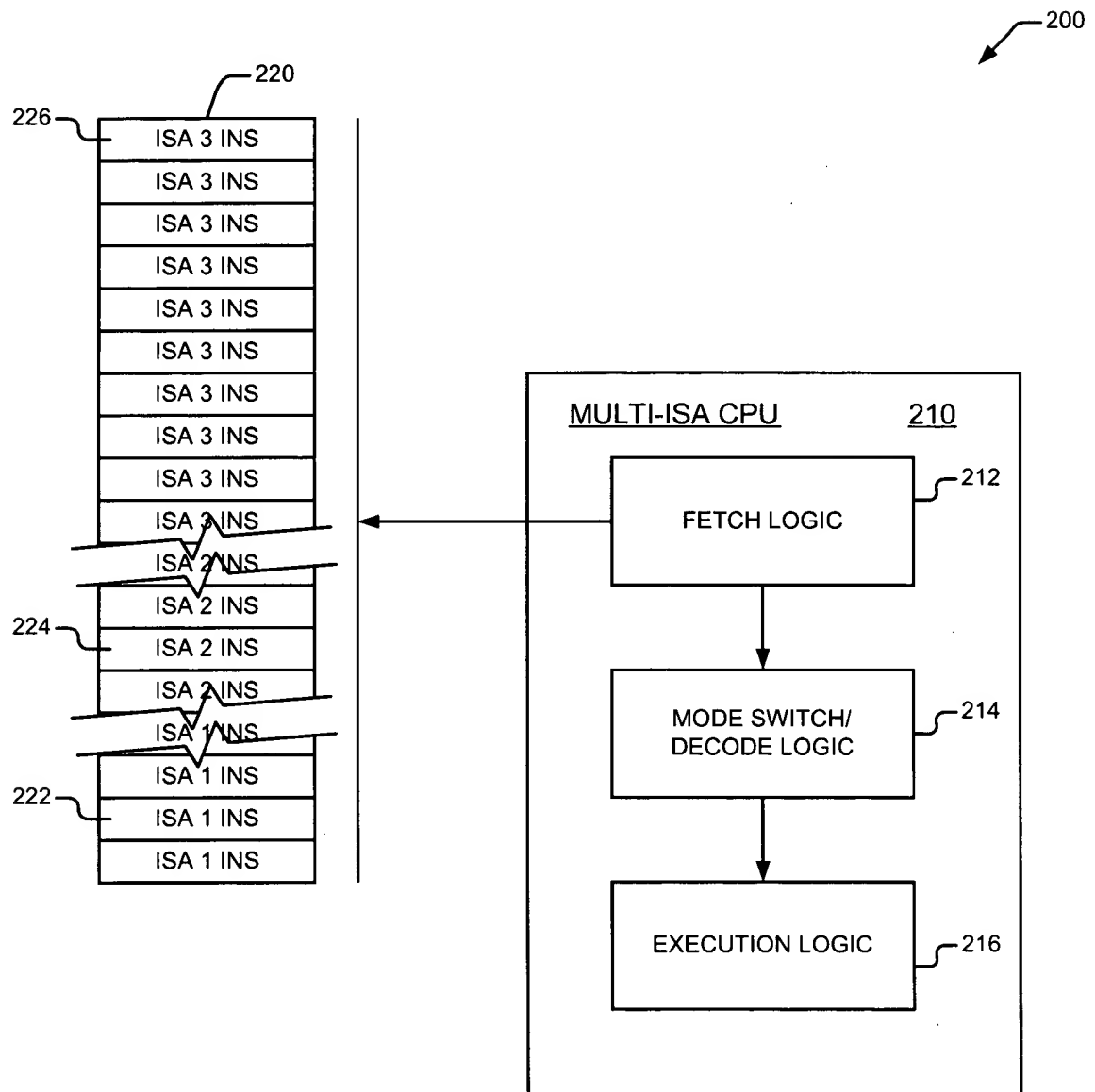


FIG. 3 (Related Art)

Present Day Multi-ISA Mode Switching/Decoding Techniques

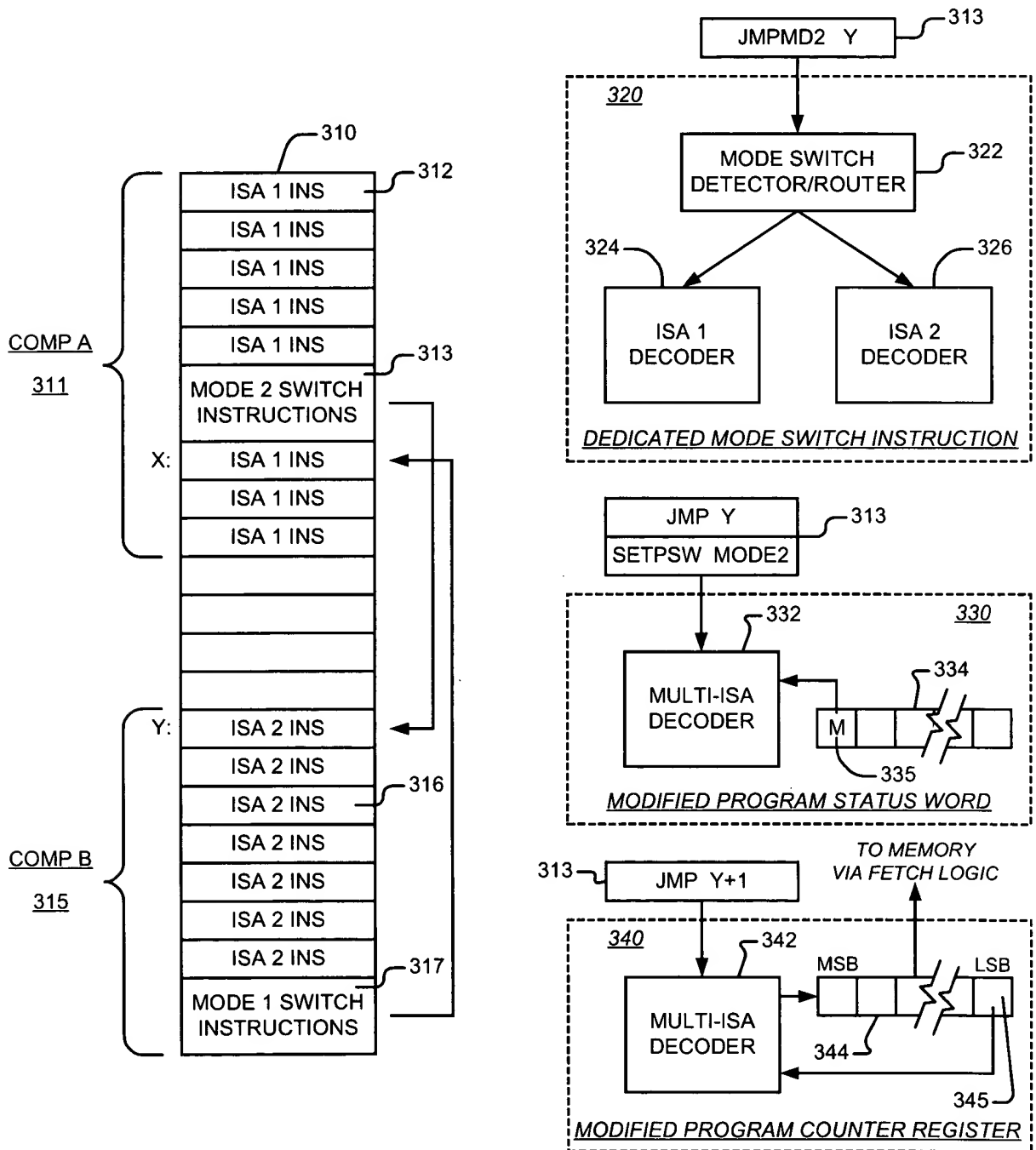


FIG. 4

Boundary Address Registers for Selection of ISA Mode

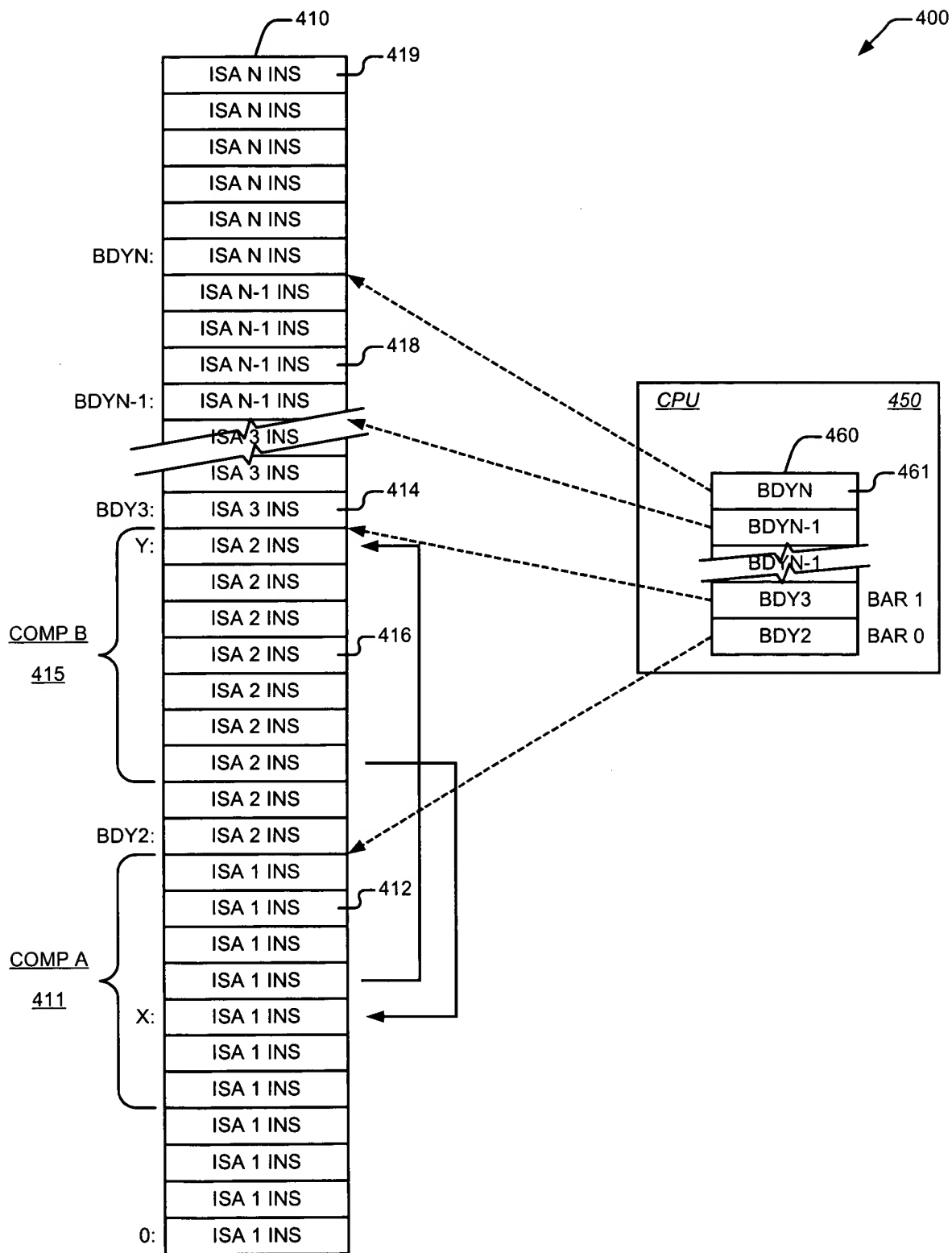


FIG. 5

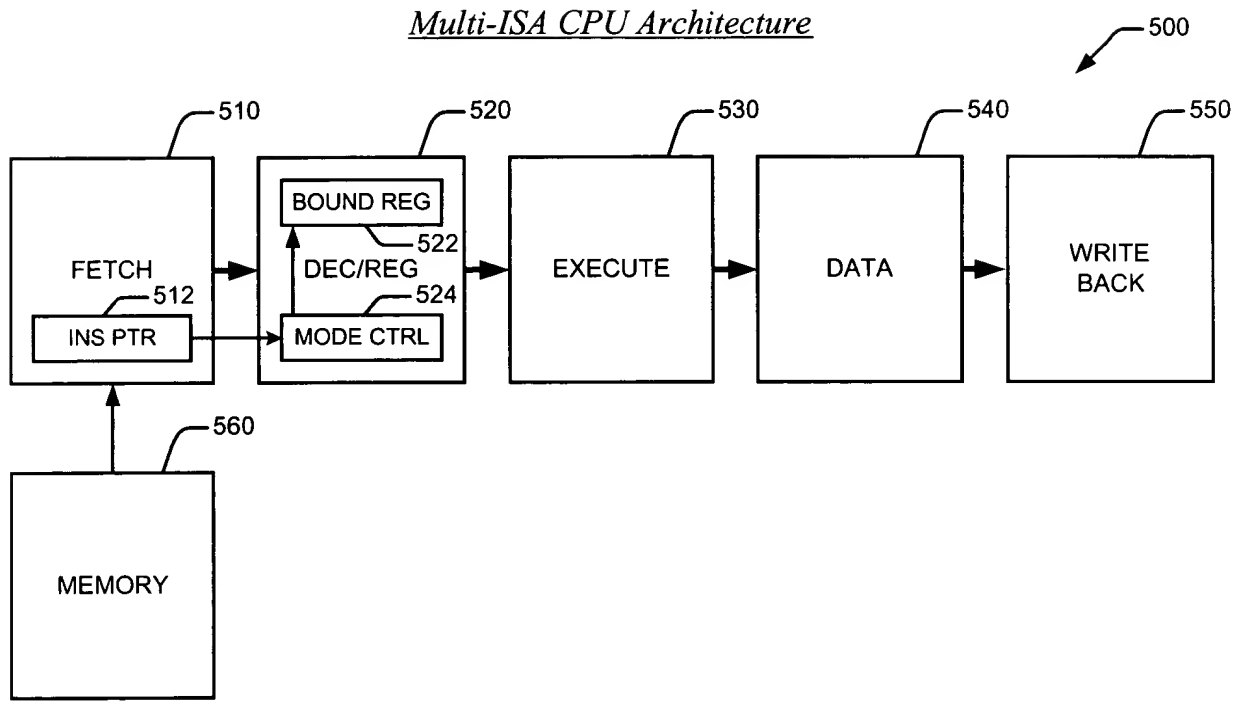


FIG. 6

Decode/Register Stage Details

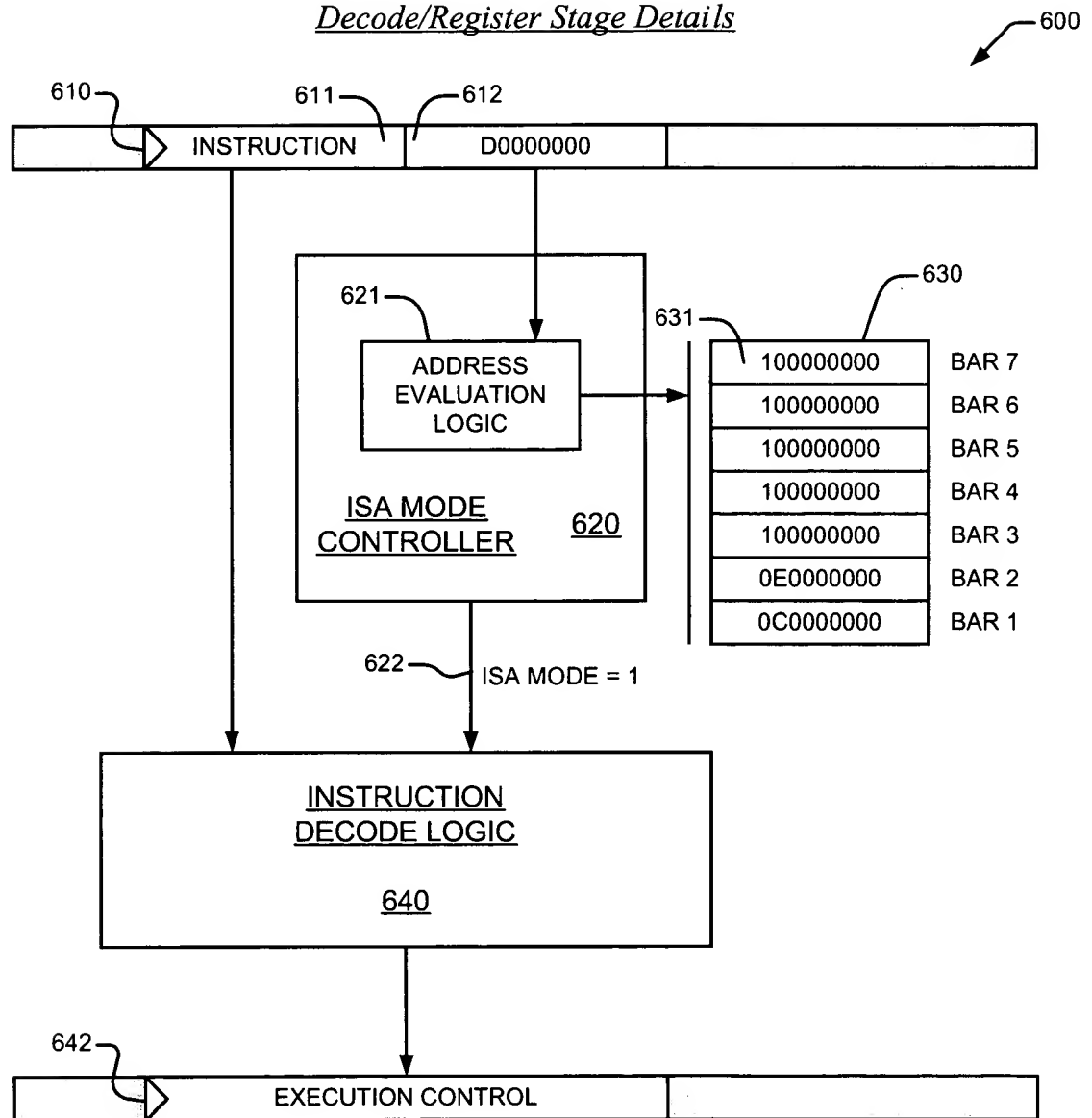


FIG. 7

Boundary Addresses Method for Selection of ISA Mode

